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Digital Filter Design for FPGA Engineers

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Digital Filter Design for FPGA Engineers Mr Kadhiem Ayob
A practical and comprehensive FIR & IIR filter design handbook written by an experienced FPGA designer and intended specifically for FPGA filter designers; both beginners and those who want to advance. Details of DSP background are set first before moving on to the core filter subjects. The focus is on both filter theory for initial design stage and details of hardware implementation and testing with ample diagrams and filter models in Matlab but avoiding unnecessary mathematical equations commonly found in software based DSP literature. The book also includes detailed chapters on using filters as rate converters such as interpolation, decimation, fractional rate converters as well as fractional delay. In the last chapter two vhdl demos are given together with their testbenches and bit true mathematical models as guidance.

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